

Convergence Analysis of Turbo Decoding of Serially Concatenated Block Codes and Product Codes

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The geometric interpretation of turbo decoding has founded a framework, and provided tools for the analysis of parallel-concatenated codes decoding. In this paper, we extend this analytical basis for the decoding of serially concatenated codes, and focus on serially concatenated product codes (SCPC) (i.e., product codes with checks on checks). For this case, at least one of the component (i.e., rows/columns) decoders should calculate the extrinsic information not only for the information bits, but also for the check bits. We refer to such a component decoder as a serial decoding module (SDM). We extend the framework accordingly and derive the update equations for a general turbo decoder of SCPC and the expressions for the main analysis tools: the Jacobian and stability matrices. We explore the stability of the SDM. Specifically, for high SNR, we prove that the maximal eigenvalue of the SDM's stability matrix approaches $d - 1$, where d is the minimum Hamming distance of the component code. Hence, for practical codes, the SDM is unstable. Further, we analyze the two turbo decoding schemes, proposed by Benedetto and Pyndiah, by deriving the corresponding update equations and by demonstrating the structure of their stability matrices for the repetition code and an SCPC code with 2×2 information bits. Simulation results for the Hamming $[(7, 4, 3)]^2$ and Golay $[(24, 12, 8)]^2$ codes are presented, analyzed, and compared to the theoretical results and to simulations of turbo decoding of parallel concatenation of the same codes.

Keywords and phrases: turbo decoding, product codes, convergence, stability.

1. INTRODUCTION

The turbo decoding algorithm is, basically, a suboptimal decoding algorithm for compound codes which were created by code concatenation. Most works on turbo codes focus on code construction, establishment of unified framework for decoding of convolutional and block turbo codes [1], adapting a turbo coding scheme for specific channels, or reducing the decoding complexity. But a comprehensive framework for the analysis of turbo decoding has yet to be found.

Richardson [2] presented a geometric interpretation of the turbo decoding process, creating analysis tools for parallel concatenation code (PCC). Based on this interpretation, [3] has checked the convergence points and trajectories of PCCs and deduced practical stopping criteria, and [4, 5] analyzed the convergence of turbo decoding of parallel-concatenated product codes (PCPC).

In this paper, we extend the analysis to turbo decoding of serially concatenated codes (SCC), and focus our attention on turbo decoding of serially concatenated product codes (SCPC) (also known as product codes with checks on checks). For this case, at least one of the components (i.e., row/column) decoders should calculate the extrinsic information of not only the information bits (as in turbo decoding of parallel-concatenated codes), but also of the check bits. We refer to such a decoder as a serial decoding module (SDM). Hence, we begin by showing how Richardson's theory [2] can be extended to apply for this decoding scheme, and how the analysis tools can be adapted accordingly. We use these tools to investigate the convergence of several variants of the decoding algorithm.

In Section 2 we describe the serial concatenation scheme, and the special case of SCPC. We review Pyndiah [6], Fang

et al. [7], and Benedetto et al. [8] variants of the iterative decoding algorithm. We then explain why the turbo decoder should include at least one SDM (which calculates the extrinsic information for the check bits as well) to take full effect of the entire code.

In Section 3, we show how Richardson's theory can be extended for serial concatenation, and specifically for the product code case. We then show how the analysis tools are adapted. First, the new turbo decoding update equations are derived. Then we derive the expressions for the Jacobian and stability matrices, and investigate their special structure for several variants of the turbo decoding algorithm. Specifically, we show that these matrices can be viewed as a generalization of the corresponding matrices for the PCPC.

In Section 4 we analyze the SDM and prove that for high SNR, the maximal eigenvalue of the SDM's stability matrix approaches $d - 1$, where d is the minimum Hamming distance of the component code. Hence, for practical codes, the SDM is unstable (note that an unstable decoding process does not necessarily imply wrong decisions at the decoder's output).

In Section 5 we derive the update equations of Pyndiah's and Benedetto's decoding schemes. We then derive and analyze the corresponding stability matrices for two simple component codes: the repetition code and a code with 2×2 information bits. This demonstrates the structure of the stability matrices and the instability of the SDM.

In Section 6 we present simulation results, which support the theoretical analysis. The simulations are performed for the Hamming $[(7, 4, 3)]^2$ and Golay $[(24, 12, 8)]^2$ codes, and compared to turbo decoding of parallel concatenation of the same codes.

2. SERIALY CONCATENATED CODES

Serial concatenation of codes is a well-known method to increase coding performance. In this scheme, the output of one component code (the *outer code*) is interleaved and encoded by a second component code (the *inner code*). Product codes (with checks on checks) are an interesting case of serially concatenated block codes [9]. They are suitable for burst and packet communication systems [7], which require short encoding-decoding delays, since they provide reasonable SNR to BER performance for relatively short code-lengths. Let C_R be an (n_R, k_R, d_R) linear code and C_C an (n_C, k_C, d_C) linear code. A linear $(n_R n_C, k_R k_C)$ product code can be formed by arranging the information bits in a $k_C \times k_R$ rectangular array, and encoding each row and column using C_R and C_C , respectively, as in Figure 1 (where x stands for the information bits, y and z for the checks on rows and columns, respectively, and w for the checks on checks).

SCPC has a minimum Hamming distance of $d = d_R d_C$, compared to PCPC with a minimum Hamming distance that is lower bounded by $d \geq d_R + d_C - 1$. SCPC may therefore match applications requiring stronger codes (at least asymptotically, i.e., for very low BER) better than those using PCPC.

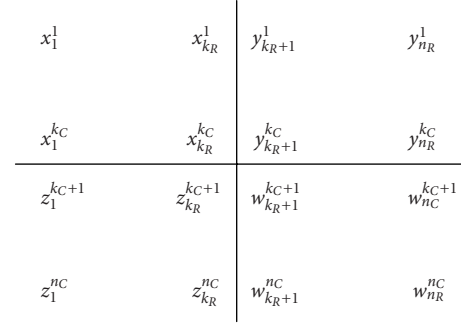


FIGURE 1: Serially concatenated product code.

We now review different decoding algorithms, which can be applied to general serial concatenation schemes (i.e., not only for product codes). Without loss of generality, we will treat the row's code as the inner code, and the column's code as the outer.

2.1. Benedetto's decoding algorithm

Benedetto et al. [8] proposed the following algorithm: the first decoder decodes the rows. Its inputs are the likelihood ratios of the received code $p(\hat{x}|x)$, $p(\hat{y}|y)$, $p(\hat{z}|z)$, $p(\hat{w}|w)$ and the extrinsic information (to be treated as the a priori information) of the data rows (x) and their column check bits (z) gained from the outer decoder $q_{C,x}^{(m-1)}$, $q_{C,z}^{(m-1)}$ (initialized to 1 at the first iteration). The decoder calculates the extrinsic information for both the rows in the x block (the information rows) and in the z block (which contains the checks on the columns of the x block, but serves as information for the $[z, w]$ rows code). We denote this extrinsic information by $q_{R,x}^{(m)}$, $q_{R,z}^{(m)}$.

The outer decoder decodes the columns. It uses the extrinsic information from the row decoder ($q_{R,x}^{(m)}$, $q_{R,z}^{(m)}$) as the channel's likelihood ratios, and sets the a priori input to be a constant 1. It then calculates the extrinsic information of the information bits $q_{C,x}^{(m)}$, as well as of the check bits of the column's code $q_{C,z}^{(m)}$. This latter decoder output ($q_{C,z}^{(m)}$) distinguishes the SCPC decoder from PCPC decoding algorithms, since extrinsic information is calculated for the check bits as well.

2.2. Pyndiah's decoding algorithm

Pyndiah [6] and later Fang et al. [7] suggested other decoding algorithms for the serial code. While these algorithms differ in their implementation details, they are both derived from a common basic scheme. In this scheme both the inner and outer decoders calculate and exchange the extrinsic information for both the information and the check bits. In this paper we will focus on this basic generic decoding scheme and consider it when we refer to Pyndiah's scheme. The following paragraph provides a detailed description of this scheme.

The inner decoder decodes the rows. Its inputs are the likelihood ratios of the received bits from the channel $p(\hat{x}|x)$,

$p(\tilde{y}|y)$, $p(\tilde{z}|z)$, $p(\tilde{w}|w)$ and the extrinsic information of x , y , z and w from the other decoding stage denoted by $q_{C,x}^{(m-1)}$, $q_{C,y}^{(m-1)}$, $q_{C,z}^{(m-1)}$, $q_{C,w}^{(m-1)}$ (treated as the a priori probability). This decoder calculates the extrinsic information of the information bits of the row's code $q_{R,x}^{(m)}$, $q_{R,z}^{(m)}$, as well as the extrinsic information of the check bits $q_{R,y}^{(m)}$ and $q_{R,w}^{(m)}$.

The outer decoder then implements the same process along the column code axis. It combines the channel likelihood ratio's $p(\tilde{x}|x)$, $p(\tilde{y}|y)$, $p(\tilde{z}|z)$, $p(\tilde{w}|w)$ and the inner decoder extrinsic information $q_{R,x}^{(m)}$, $q_{R,y}^{(m)}$, $q_{R,z}^{(m)}$, $q_{R,w}^{(m)}$ as its inputs, and calculates the extrinsic information of the information bits of the column's code $q_{C,x}^{(m)}$ and $q_{C,y}^{(m)}$, as well of that of the check bits $q_{C,z}^{(m)}$ and $q_{C,w}^{(m)}$.

The optimal component decoder is the Log-MAP decoder, and it is the decoder we will consider in our work even though it is not the most computationally efficient. Both Pyndiah and Fang et al. proposed the usage of more computationally efficient suboptimal decoders: a modified Chase algorithm or an augmented list decoding (which was similarly proposed in [10]), respectively. Pyndiah also multiplied the exchanged extrinsic information by a set of restraining factors, which we will introduce to our model as well.

2.3. The reasoning behind SDM

The common attribute of all the SCPC decoding schemes we analyze, is the computation of the extrinsic information of not only the information bits (as for parallel-concatenated codes) but also of the check bits in at least one decoder. Of course, it is possible to decode without such a decoder, but here we explain that such a decoding scheme would not take full advantage of the entire code (this particularity was also pointed out in [11]). We will designate such a component decoder as SDM and a decoding block that calculates the extrinsic information of only the information bits as a parallel decoding module (PDM).

We consider applying the parallel decoding scheme to an SCPC code, using PDM blocks. We will use the PDM decoders to decode any part of the code they can decode (even if it is not part of a common parallel decoding scheme).

At the first iteration, the row decoder uses $p(\tilde{x}|x)$, $p(\tilde{y}|y)$ to compute $q_{R,x}^{(1)}$, and may use $p(\tilde{z}|z)$, $p(\tilde{w}|w)$ to compute $q_{R,z}^{(1)}$. The column decoder uses $p(\tilde{x}|x)$, $p(\tilde{z}|z)$, $q_{R,x}^{(1)}$, $q_{R,z}^{(1)}$ to compute $q_{C,x}^{(1)}$ and may use $p(\tilde{y}|y)$, $p(\tilde{w}|w)$ to compute $q_{C,y}^{(1)}$. Note that we decoded *all* the rows and *all* the columns rather than only compute $q_{R,x}^{(1)}$ and $q_{C,x}^{(1)}$ as in the classical parallel decoding scheme.

At the m th iteration the row decoder uses $p(\tilde{x}|x)$, $p(\tilde{y}|y)$, $q_{C,x}^{(m-1)}$, $q_{C,y}^{(m-1)}$ to compute $q_{R,x}^{(m)}$ and $p(\tilde{z}|z)$, $p(\tilde{w}|w)$ to compute $q_{R,z}^{(m)}$. The column decoder uses $p(\tilde{x}|x)$, $p(\tilde{z}|z)$, $q_{R,x}^{(m)}$, $q_{R,z}^{(m)}$ to compute $q_{C,x}^{(m)}$ and $p(\tilde{y}|y)$, $p(\tilde{w}|w)$ to compute $q_{C,y}^{(m)}$.

We conclude that the updates of $q_{R,z}^{(m)}$ and $q_{C,y}^{(m)}$ depend only on the channel probabilities, and are independent of $q_{R,x}^{(m)}$, $q_{C,x}^{(m)}$. Therefore, they will remain constant: $q_{R,z}^{(m)} = q_{R,z}^{(1)}$ for all m , $q_{C,y}^{(m)} = q_{C,y}^{(1)}$ for all m . Hence, the contributions of

the checks on checks portion (i.e., the extrinsic information of the checks on rows and of the checks on the columns) do not affect the iterative process. This makes such an algorithm to be degenerate.

However, using a component decoder, that computes the extrinsic information for all the code bits (i.e., including the check bits), could tie the updates of $q_{R,z}^{(m)}$ and $q_{C,y}^{(m)}$ with their values in the previous iteration and with $q_{R,x}^{(m)}$, $q_{C,x}^{(m)}$. We thus conclude that at least one of the component decoders should be an SDM.

3. ANALYSIS OF TURBO DECODING OF SERIALLY CONCATENATED PRODUCT CODES

Our analysis is based on the geometric representation of turbo codes, formulated by Richardson in [2], in which tools and conditions were developed for analyzing the stability of the fixed points of the algorithm, their uniqueness, and their proximity to maximum likelihood decoding. This framework addressed parallel concatenation of codes, and was used in the analysis of PCPC [4, 5]. As was demonstrated in the previous section, the turbo decoding of SCPC requires the computation of an additional element, which is the extrinsic information of the check bits. Hence, we first show how Richardson's theory can be extended for this case.

3.1. Notations

We begin with the case of a PDM decoder. Consider the sequence of all possible k -bit combinations $\tilde{b}^0, \tilde{b}^1, \dots, \tilde{b}^{2^k-1}$ which is enumerated as follows:

$$\begin{aligned} \tilde{b}^0 &= (0, 0, \dots, 0)^T, & \tilde{b}^1 &= (1, 0, \dots, 0)^T, \\ \tilde{b}^2 &= (0, 1, 0, \dots, 0)^T, \dots, \\ \tilde{b}^k &= (0, \dots, 0, 1)^T, & \tilde{b}^{k+1} &= (1, 1, 0, \dots, 0)^T, \dots, \\ \tilde{b}^{2^k-1} &= (1, \dots, 1)^T. \end{aligned} \quad (1)$$

A *density* p assigns a nonnegative measure to each of the \tilde{b}^i 's, proportional to its *probability density*. For convenience, we will assume that densities are strictly positive. Densities p and q are *equivalent* [2] (and thus belong to the same *equivalence class*) if they determine the same probability density. Since turbo decoding (with maximum likelihood component decoders) uses only the ratios between (probability) densities, it is invariant under equivalence. Therefore, we can choose a particular representative from each equivalence class. Richardson chose to use the density with $p(\tilde{b}^0) = p(0, 0, \dots, 0) = 1$. By taking the logarithm of the representative densities, we define Φ to be the set of *log-densities* P , such that $P(\tilde{b}^0) = 0$ (in the sequel, upper case letters will denote log-densities, and lower case letters will denote densities).

Given a linear systematic block code $C(n, k, d)$, let H_i $i = 1, \dots, k$ denote the set of all binary strings b whose i th bit is 1, and \bar{H}_i denotes the set of all strings whose i th bit is 0. Now, if we denote by Y the concatenation of the systematic

code portion x and the checks portion y : $Y = [x \ y]$, then for each log-density P , we can calculate the bitwise log likelihood values, by using the map $\tilde{\pi}_{\text{PDM}}(P) : \mathbb{R}^{2^k-1} \mapsto \mathbb{R}^k$:

$$\begin{aligned} \tilde{\pi}_{\text{PDM}}(P)(\tilde{b}^i) &= \log \frac{\sum_{Y:Y_i=1} P(\tilde{Y}|Y)}{\sum_{Y:Y_i=0} P(\tilde{Y}|Y)} = \log \frac{\sum_{b \in H_i} P(\tilde{b}^i)}{\sum_{b \in \bar{H}_i} P(\tilde{b}^i)} \quad (2) \\ &= \text{LLR}(Y_i) \quad \forall i = 1, \dots, k, \end{aligned}$$

where LLR is the log-likelihood ratio. Richardson gives $\tilde{\pi}_{\text{PDM}}(\cdot)$ a geometric interpretation, as the intersection of the surface of all log-densities having the same bitwise marginal distributions, with the space of bitwise independent log-densities.

The above definition of $\tilde{\pi}_{\text{PDM}}(\cdot)$ addresses the computation of the LLR of the information bits only. As was discussed in the previous section, an SCPC decoder should contain at least one SDM decoder, which calculates also the extrinsic information of the code's check bits. Hence, we now extend Richardson's theory for this case.

First, we extend the set of the sequences b , and include all possible n -bit combinations $b^0, b^1, \dots, b^{2^n-1}$ which is enumerated as follows:

$$\begin{aligned} b^0 &= (0, 0, \dots, 0)^T, & b^1 &= (1, 0, \dots, 0)^T, \\ b^2 &= (0, 1, 0, \dots, 0)^T, & & \\ b^n &= (0, \dots, 0, 1)^T, & b^{n+1} &= (1, 1, 0, \dots, 0)^T, \dots, \\ b^{2^n-1} &= (1, \dots, 1)^T. \end{aligned} \quad (3)$$

We choose (without loss of generality) to number the code bits according to their arrangement by rows: $b = (x^{r_1}, y^{r_1}, \dots, x^{r_{k_c}}, y^{r_{k_c}}, z^{r_1}, w^{r_1}, \dots, z^{r_{n_c-k_c}}, w^{r_{n_c-k_c}})$, where $x^{r_i}, y^{r_i}, z^{r_i}, w^{r_i}$ denote the i th row of x, y, z, w , respectively. Let B denote a $2^n \times n$ matrix containing all the sequences: $B = (b^0, b^1, \dots, b^{2^n-1})^T$, and let B_C denote a $2^k \times n$ matrix containing all the codewords in the same order as B . Define $\bar{B}_C = \mathbf{1}_{2^k \times n} - B_C$ (where $\mathbf{1}_{2^k \times n}$ denotes the all ones matrix of size $2^k \times n$). Since now some of the sequences do not belong to the code, we define $H_i^C \ i = 1, \dots, n$ as the set of binary strings b whose i th bit is 1, and belong to the code C (and \bar{H}_i^C as the set of all strings whose i th bit is 0 and which belong to C):

$$H_i^C = \{b \in H \cap C : b \geq b^i\}, \quad (4)$$

where H is the n -dimensional hypercube (the set of binary vectors of length n), C is the set of all the code words, and \geq is meant componentwise (note that b^i is the sequence with 1 in the i th position, and 0 in all other positions).

Denote by Y the codewords of the row code, generated by concatenation of the systematic code portion x , and the checks portion y : $Y = [x \ y]$. For each log-density P , we can calculate the bitwise log likelihood values, by using the map

$\tilde{\pi}(P) : \mathbb{R}^{2^n-1} \mapsto \mathbb{R}^n$:

$$\begin{aligned} \tilde{\pi}(P)(b^i) &= \log \frac{\sum_{b \in H_i^C} e^{P(b)}}{\sum_{b \in \bar{H}_i^C} e^{P(b)}} = \log \frac{\sum_{b \in H_i^C} P(b)}{\sum_{b \in \bar{H}_i^C} P(b)} \\ &= \log \frac{\sum_{Y:Y_i=1} \text{Pr}(\tilde{Y}|Y)}{\sum_{Y:Y_i=0} \text{Pr}(\tilde{Y}|Y)} \quad (5) \\ &= \text{LLR}(Y_i) \quad \forall i = 1, \dots, n. \end{aligned}$$

This keeps the same definition as in [2] except that the calculation has been generalized for every code bit $i = 1, \dots, n$. Note that $\tilde{\pi}(P)$, which is the vector $(\tilde{\pi}(P)(b^1), \dots, \tilde{\pi}(P)(b^n))^T$, is the vector of bitwise log-likelihood values associated with P .

3.2. Turbo decoding of SCPC

We now use the new definitions to build a new set of Richardson's update equations. The turbo decoder depends on the equivalence classes of $p(\tilde{x}|x), p(\tilde{y}|y), p(\tilde{z}|z), p(\tilde{w}|w)$. Let $P_{\tilde{x}|x}, P_{\tilde{y}|y}, P_{\tilde{z}|z}, P_{\tilde{w}|w}$ represent these equivalence classes in Φ . We define

$$P_{\tilde{x}|x}^{C_R}(b^i) = \log \prod_{j=1}^{k_R} p(\tilde{x}_j | b^i(j)), \quad b^i \in C_R, \quad (6a)$$

$$P_{\tilde{y}|y}^{C_R}(b^i) = \log \prod_{j=k_R+1}^{n_R} p(\tilde{y}_j | b^i(j)), \quad b^i \in C_R. \quad (6b)$$

Hence, the probability of each codeword of the first k_c rows can be written as $[P_{\tilde{x}|x}^{C_R}; P_{\tilde{y}|y}^{C_R}] \cdot P_{\tilde{z}|z}^{C_R} P_{\tilde{w}|w}^{C_R}$ are defined similarly.

Let $Q_{R,x}^{(m)}, Q_{R,y}^{(m)}, Q_{R,z}^{(m)}$, and $Q_{R,w}^{(m)}$ denote the extrinsic information of x, y, z , and w blocks, respectively, extracted by the row decoder at the m th iteration. Let $Q_{C,x}^{(m)}, Q_{C,y}^{(m)}, Q_{C,z}^{(m)}$, and $Q_{C,w}^{(m)}$ represent the outputs of the column decoder in the same manner. $Q_{R,x}^{(m)}$ is similarly defined to (6), for example, $Q_{R,x}^{(m)}$ is the extrinsic information of the information bits (x) extracted by the row decoder, and is defined as $Q_{R,x}(b^i) = \log \prod_{j=1}^{k_R} q_{R,x^j}(b^i(j)) \ b^i \in C_R$. The new update equations become as follows (refer to [2] for the PCPC case):

$$\begin{aligned} [Q_{R,x}^{(m)}; Q_{R,y}^{(m)}] &\leftarrow \tilde{\pi} \left([P_{\tilde{x}|x}^{C_R}; P_{\tilde{y}|y}^{C_R}] + [Q_{C,x}^{(m-1)}; Q_{C,y}^{(m-1)}] \right) \\ &\quad - \left([P_{\tilde{x}|x}^{C_R}; P_{\tilde{y}|y}^{C_R}] + [Q_{C,x}^{(m-1)}; Q_{C,y}^{(m-1)}] \right), \end{aligned} \quad (7a)$$

$$\begin{aligned} [Q_{R,z}^{(m)}; Q_{R,w}^{(m)}] &\leftarrow \tilde{\pi} \left([P_{\tilde{z}|z}^{C_R}; P_{\tilde{w}|w}^{C_R}] + [Q_{C,z}^{(m-1)}; Q_{C,w}^{(m-1)}] \right) \\ &\quad - \left([P_{\tilde{z}|z}^{C_R}; P_{\tilde{w}|w}^{C_R}] + [Q_{C,z}^{(m-1)}; Q_{C,w}^{(m-1)}] \right), \end{aligned} \quad (7b)$$

$$\begin{aligned} [Q_{C,x}^{(m)}; Q_{C,z}^{(m)}] &\leftarrow \tilde{\pi} \left([P_{\tilde{x}|x}^{C_C}; P_{\tilde{z}|z}^{C_C}] + [Q_{R,x}^{(m)}; Q_{R,z}^{(m)}] \right) \\ &\quad - \left([P_{\tilde{x}|x}^{C_C}; P_{\tilde{z}|z}^{C_C}] + [Q_{R,x}^{(m)}; Q_{R,z}^{(m)}] \right), \end{aligned} \quad (7c)$$

$$\begin{aligned} [Q_{C,y}^{(m)}; Q_{C,w}^{(m)}] &\leftarrow \tilde{\pi} \left([P_{\tilde{y}|y}^{C_C}; P_{\tilde{w}|w}^{C_C}] + [Q_{R,y}^{(m)}; Q_{R,w}^{(m)}] \right) \\ &\quad - \left([P_{\tilde{y}|y}^{C_C}; P_{\tilde{w}|w}^{C_C}] + [Q_{R,y}^{(m)}; Q_{R,w}^{(m)}] \right). \end{aligned} \quad (7d)$$

It is also interesting to observe the derivation of an SDM Jacobian for a row decoder that calculates the extrinsic information of only the information bits (as done by a PCPC turbo decoder). We get the following structure:

$$J_{x,z,y,w}^{R,i} = \left(\begin{array}{ccc|c} j_{1,1}^{R,i} & \cdots & j_{1,k_R}^{R,i} & 0 \\ \vdots & \ddots & \vdots & 0 \\ j_{k_C,1}^{R,i} & \cdots & j_{k_C,k_R}^{R,i} & 0 \\ \hline 0 & & & 0 \end{array} \right), \quad (19)$$

$$j_{m,n}^{R,i} = \begin{cases} j_{m,n}^{R,i(\text{PCPC})}, & 1 \leq m, n \leq k_C, k_R, \\ 0, & k_C + 1, k_R + 1 \leq m, n \leq n_C, n_R, \end{cases} \quad (20)$$

where $j_{m,n}^{R,i(\text{PCPC})}$ is the corresponding Jacobian element of the PCPC decoder. Hence, the Jacobian (and stability) matrices of the SCPC turbo decoder are a generalization of the corresponding matrices of the PCPC decoder.

4. STABILITY OF SDM-TYPE DECODER AT ASYMPTOTICALLY HIGH SNR

In [3] it was shown that the fixed points of PCPC turbo decoder are stable at high SNR. This section examines the stability of the SDM of SCPC at high SNRs and shows that its fixed points are inherently unstable for practical codes. We prove the following claim.

Claim 1. The maximal eigenvalue of the SDM's stability matrix approaches $d - 1$ (where d is the minimum Hamming distance of the component code) at an asymptotically high SNR.

Proof. To prove the claim, examine the stability matrix at high SNR. Calculating the actual eigenvalues might be impractical for arbitrary matrix. But the maximal eigenvalue has a well-known upper bound [12],

$$\max_i \sum_j |S_{i,j}| \geq \max_k \lambda_k. \quad (21)$$

We can reevaluate this expression in the following way:

$$\begin{aligned} |\lambda_{\max}| &\leq \max_i \left(\sum_j |(S)_{ij}| \right) = \max_i \left(\sum_j |(J_P^C)_{i,j}| \right) - 1 \\ &= \max_i \left(\sum_j \left| \frac{\sum_{b \in H_i^c \cap H_j^c} p(b)}{\sum_{b \in H_i^c} p(b)} - \frac{\sum_{b \in \bar{H}_i^c \cap H_j^c} p(b)}{\sum_{b \in \bar{H}_i^c} p(b)} \right| \right) - 1 \\ &\leq \max_i \left(\sum_j \left(\left| \frac{\sum_{b \in H_i^c \cap H_j^c} p(b)}{\sum_{b \in H_i^c} p(b)} \right| \right) \right. \\ &\quad \left. + \sum_j \left| \frac{\sum_{b \in \bar{H}_i^c \cap H_j^c} p(b)}{\sum_{b \in \bar{H}_i^c} p(b)} \right| \right) - 1 \\ &= \max_i (A_i + B_i) - 1. \end{aligned} \quad (22)$$

A_i and B_i are positive expressions defined as follows:

$$A_i \equiv \frac{\sum_j \sum_{b \in H_i^c \cap H_j^c} p(b)}{\sum_{b \in H_i^c} p(b)} = \frac{\sum_{b \in H_i^c} w_H(b) \cdot p(b)}{\sum_{b \in H_i^c} p(b)}, \quad (23a)$$

$$\begin{aligned} B_i &\equiv \frac{\sum_j \sum_{b \in \bar{H}_i^c \cap H_j^c} p(b)}{\sum_{b \in \bar{H}_i^c} p(b)} = \frac{\sum_{b \in \bar{H}_i^c} w_H(b) \cdot p(b)}{\sum_{b \in \bar{H}_i^c} p(b)} \\ &= \frac{\sum_{b \in \bar{H}_i^c, b \neq (00 \dots 0)} w_H(b) \cdot p(b)}{\sum_{b \in \bar{H}_i^c} p(b)}, \end{aligned} \quad (23b)$$

where $w_H(b)$ denotes the Hamming weight of the bit sequence b .

Without loss of generality, assume that the all-zeros codeword was transmitted. At asymptotically high SNR, the error probability (for AWGN channel) decreases exponentially with the number of errors: $p(b) \propto \exp(-w_H(b))$. Therefore, the above expression will converge to a limit.

For A_i , the most dominant term(s) in the numerator and the denominator is the codeword(s) with the lowest weight, that is, with the code's minimum Hamming distance (d). For B_i the all-zeros codeword is the most probable and it appears only in the denominator (since $w_H(b) = 0$ if b is the all-zeros code word):

$$A_i \xrightarrow{\text{SNR} \rightarrow \infty} \min(w_H(b)) = d, \quad (24a)$$

$$B_i \xrightarrow{\text{SNR} \rightarrow \infty} 0. \quad (24b)$$

Substituting these limits in the expression for the stability matrix we get that for each row i ,

$$\sum_j S_{i,j} \xrightarrow{\text{SNR} \rightarrow \infty} d - 1 \quad \forall j. \quad (25)$$

Since, at the limit, the sum of the elements along every row of the matrix is constant, it will become an eigenvalue (with an eigenvector of $[1, \dots, 1]$). Therefore, the stability matrix of the decoder is unstable at high SNR for any code with $d \geq 2$. Equation (22) proves that this is the upper limit as well:

$$\max_k |\lambda_k| \xrightarrow{\text{SNR} \rightarrow \infty} d - 1, \quad (26)$$

and this proves the claim. \square

A PCPC decoder always has at least a single fixed point [2], and its stability matrix was derived in the context of that point. That is, assuming the decoder is in the fixed point vicinity, the stability matrix indicates if and how fast the decoding will converge to the point. However, for an SDM-type decoder, we did not prove that a fixed point must exist. Hence, in the analysis of SDMs, the Jacobian and stability matrices are mainly viewed as the derivative of the update equations with respect to the extrinsic information. Hence, we conclude that the maximal eigenvalue of the stability matrix and its related eigenvector indicate that the decoding process drives the extrinsic information to infinity in the direction of the selected codeword. That is, the SDM increases the density in a direction supporting the most likely codeword.

Note that in [13] it was shown that the slope in the graph of the density evolution SNR for serially concatenated codes is related to the same value: $d - 1$ (when the SNR is high). Here, a similar result is derived analytically. We believe that both results are connected and reflect similar phenomena.

In the case of turbo decoding of SCPC, each row (and column) has its own Jacobian and stability submatrices. Each of these stability submatrices has a maximal eigenvalue of $d - 1$, and an all-ones corresponding eigenvector, for high SNR. Hence, the stability matrix of the rows (columns) decoder will have n eigenvalues, all of which converge to the limit of $d - 1$ at high SNR (the eigenvalues of a block matrix are a union of the eigenvalues of all its submatrices).

The inherent instability of the SDM (demonstrated at high SNR) can be stabilized through other elements in the decoding process. A possible stabilizing approach is the multiplication of the extrinsic information by restraining factors through the update equations (as Pyndiah implemented as part of his decoding system [6]). Note that knowing the eigenvalue's upper bound, one can ensure stability using this method. Another approach to stabilize the resulting densities is to apply a (generally stable) decoder which calculates the extrinsic information of only the information bits, for one component code, along with an SDM decoder for the other code—as was proposed by Benedetto et al. [8].

It is important to note that an unstable decoding process, in the sense we have just shown, does not necessarily imply wrong decisions at the decoder's output. The instability of the decoder merely increases the density values. It does not change the decisions made by the decoder. The extrinsic information Q is a log likelihood ratio of the form $\log p(x = 1|\text{data})/p(x = 0|\text{data})$. If $p(x = 1|\text{data}) \rightarrow 1$ (or $p(x = 1|\text{data}) \rightarrow 0$), then $Q \rightarrow \infty$ (or $Q \rightarrow -\infty$). Hence, the instability of Q actually means that the decoder becomes more confident that $x = 1$ (or $x = 0$), which is reasonable as the SNR improves. Indeed, many of our simulations show that the SDM is increasing the extrinsic values of the correct word, instead of letting it converge to some constant.

5. STABILITY ANALYSIS OF SOME SCPC DECODING ALGORITHMS

In the previous section the stability of a single SDM was analyzed. A full decoding scheme has two decoding stages. For an SCPC decoding scheme, at least one of these decoders is an SDM. This section investigates the entire decoding process, using the formalized representation developed in the previous section. Specifically, we investigate the decoding algorithms proposed by Benedetto and Pyndiah by deriving the corresponding update equations. We then derive and analyze the stability matrices for two simple component codes: the repetition code and a code with 2×2 information bits. By this we demonstrate the structure of the stability matrices and the instability of the SDM.

5.1. Benedetto's decoding scheme

The update equations for Benedetto et al. [8] algorithm are

$$\begin{aligned} [Q_{R,x}^{(m)}; 0] &\leftarrow \tilde{\pi}_{C_{C_R}} \left([P_{\hat{x}|x}^{C_R}; P_{\hat{y}|y}^{C_R}] + [Q_{C,x}^{(m-1)}; 0] \right) \\ &\quad - \left([P_{\hat{x}|x}^{C_R}; 0] + [Q_{C,x}^{(m-1)}; 0] \right), \end{aligned} \quad (27a)$$

$$\begin{aligned} [Q_{R,z}^{(m)}; 0] &\leftarrow \tilde{\pi}_{C_{C_R}} \left([P_{\hat{z}|z}^{C_R}; P_{\hat{w}|w}^{C_R}] + [Q_{C,z}^{(m-1)}; 0] \right) \\ &\quad - \left([P_{\hat{z}|z}^{C_R}; 0] + [Q_{C,z}^{(m-1)}; 0] \right), \end{aligned} \quad (27b)$$

$$\begin{aligned} [Q_{C,x}^{(m)}; Q_{C,z}^{(m)}] &\leftarrow \tilde{\pi}_{C_{C_C}} \left([Q_{R,x}^{(m)}; Q_{R,z}^{(m)}] \right) \\ &\quad - \left([Q_{R,x}^{(m)}; Q_{R,z}^{(m)}] \right). \end{aligned} \quad (27c)$$

These equations are based on the general structure described by (7), modified in accordance with Benedetto's decoding scheme: the first two equations (27a) and (27b) express the first decoding stage, the row (inner) decoding of both the information (x) and checks on the columns (z). This is a PDM decoder, and its output contains extrinsic information of its information bits only, hence both these equations have the form of (9a).

The third equation (27c) expresses the second (outer) decoding stage: column decoding of the information rows. This equation would have been identical to equation (7c), except that Benedetto's decoding scheme does not use the a priori density probabilities here. Note that this is an SDM decoder, whose output contains the extrinsic information of both its information and check bits.

The maximal eigenvalue of S is smaller than or equal to the product of the maximal eigenvalues of S_R and S_C . A sufficient condition for the stability of S is that this product will be less than 1. Given our previous analysis for a high SNR, S_C eigenvalues are limited to $d_C - 1$, so a sufficient stability condition for S is that the eigenvalues of S_R are smaller than $(d_C - 1)^{-1}$. Since under high SNR conditions, the eigenvalues of the inner decoder (a PDM decoder) converge to 0 in probability [3], it satisfies the stability condition. Hence, Benedetto's decoding algorithm is stable for high SNR's.

The row decoder has a stability matrix with n_C square submatrices $J_{x,y}^{C_R,i}$ of size k_R on the main diagonal. However, the second decoding stage has the same structure except that it has k_R square submatrices $J_{x,y}^{C_C,i}$ of size n_C on its main diagonal.

Decoding stability of an SCPC with a repetition code

As a simple example, consider an SCPC with a repetition code as its component rows and columns codes. Assume the code has a single data bit, which is repeated d_R times in each row, and d_C times in each column. The generator matrix for the component codes has the following form:

$$G = \begin{bmatrix} 1 & 1 & \cdots & 1 \\ \underbrace{\hspace{1.5cm}}_d \end{bmatrix}. \quad (28)$$

We will now examine the stability matrices. S_R has $n_C = d_C$ square blocks, with the structure of (18). Since $k_R = 1$, it can be easily shown that the submatrices are the all-zero matrix of size 1×1 . Thus S_R is the zero matrix and has zero as a multiple eigenvalue. As for S_C , it has only one square block (we decode a single column), with a size of $n_C = d_C$. Since there are only two codewords (all ones and all zeros), all the matrix elements equal 1, except for the all-zero main diagonal:

$$S_R = \begin{bmatrix} 0 & \cdots & 0 \\ \vdots & \ddots & \vdots \\ \underbrace{0 \cdots 0}_1 \\ \vdots & \ddots & \vdots \\ \underbrace{0 \cdots 0}_{d_C} \end{bmatrix}, \quad (29a)$$

$$S_C = \begin{bmatrix} 0 & 1 & \cdots & \cdots & 1 \\ 1 & 0 & 1 & \cdots & \cdots \\ \cdots & 1 & 0 & 1 & \cdots \\ \cdots & \cdots & 1 & 0 & 1 \\ \underbrace{1 \cdots \cdots 1 \ 0}_1 \end{bmatrix}. \quad (29b)$$

The maximal eigenvalue of S_C is $d_C - 1$, therefore S_C is unstable for any SNR. Yet, the overall process is stable, due to the stability of S_R .

A code with 2×2 information bits

As a second example consider a column (outer) encoder with two data bits and a single check bit (parity), and a row (inner) encoder with two data bits and arbitrary number of check bits. The stability matrices are

$$S_R = \begin{bmatrix} 0 & \mu_{1,2} & 0 & 0 & 0 & 0 \\ \mu_{2,1} & 0 & 0 & 0 & 0 & 0 \\ \hline 0 & 0 & 0 & \mu_{3,4} & 0 & 0 \\ 0 & 0 & \mu_{4,3} & 0 & 0 & 0 \\ \hline 0 & 0 & 0 & 0 & 0 & \mu_{6,5} \\ 0 & 0 & 0 & 0 & \mu_{5,6} & 0 \end{bmatrix}, \quad (30a)$$

$$S_C = \begin{bmatrix} 0 & \gamma_{1,2} & \gamma_{1,3} & 0 & 0 & 0 \\ \gamma_{2,1} & 0 & \gamma_{2,3} & 0 & 0 & 0 \\ \hline \gamma_{3,1} & \gamma_{3,2} & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & \gamma_{4,5} & \gamma_{4,6} \\ 0 & 0 & 0 & \gamma_{5,4} & 0 & \gamma_{5,6} \\ 0 & 0 & 0 & \gamma_{6,4} & \gamma_{6,5} & 0 \end{bmatrix}. \quad (30b)$$

S_R is stable for any row code and SNR as was proven in [5]. We have shown the maximal eigenvalues of S_C to converge to 1 ($= d_C - 1$) in high SNR, causing the second stability matrix to be marginally stable. Thus the overall decoder is stable.

5.2. Pyndiah's decoding scheme

We will now analyze the stability of Pyndiah's and Fang's decoding schemes. The scheme has SDM-type decoders for both the row and column decoders. Pyndiah [6] also suggested the usage of a set of restraining factors $\alpha(m)$, by which the extrinsic information should be multiplied in each iteration. The set of factors begins with a value of zero for the first iteration, and gradually increases to one.

In our notations, the update equations of these schemes are as follows (note that here we use the optimal MAP decoder, where Pyndiah and Fang used suboptimal decoders):

$$\begin{aligned} [Q_{R,x}^{(m)}, Q_{R,y}^{(m)}] &\leftarrow \tilde{\pi} \left([P_{\hat{x}|x}^{C_R}, P_{\hat{y}|y}^{C_R}] + \alpha(m) \cdot [Q_{C,x}^{(m-1)}; Q_{C,y}^{(m-1)}] \right) \\ &\quad - \left([P_{\hat{x}|x}^{C_R}, P_{\hat{y}|y}^{C_R}] + \alpha(m) \cdot [Q_{C,x}^{(m-1)}; Q_{C,y}^{(m-1)}] \right), \end{aligned} \quad (31a)$$

$$\begin{aligned} [Q_{R,z}^{(m)}, Q_{R,w}^{(m)}] &\leftarrow \tilde{\pi} \left([P_{\hat{z}|z}^{C_R}, P_{\hat{w}|w}^{C_R}] + \alpha(m) \cdot [Q_{C,z}^{(m-1)}; Q_{C,w}^{(m-1)}] \right) \\ &\quad - \left([P_{\hat{z}|z}^{C_R}, P_{\hat{w}|w}^{C_R}] + \alpha(m) \cdot [Q_{C,z}^{(m-1)}; Q_{C,w}^{(m-1)}] \right), \end{aligned} \quad (31b)$$

$$\begin{aligned} [Q_{C,x}^{(m)}, Q_{C,z}^{(m)}] &\leftarrow \tilde{\pi} \left([P_{\hat{x}|x}^{C_C}, P_{\hat{z}|z}^{C_C}] + \alpha(m) \cdot [Q_{R,x}^{(m)}; Q_{R,z}^{(m)}] \right) \\ &\quad - \left([P_{\hat{x}|x}^{C_C}, P_{\hat{z}|z}^{C_C}] + \alpha(m) \cdot [Q_{R,x}^{(m)}; Q_{R,z}^{(m)}] \right), \end{aligned} \quad (31c)$$

$$\begin{aligned} [Q_{C,y}^{(m)}, Q_{C,w}^{(m)}] &\leftarrow \tilde{\pi} \left([P_{\hat{y}|y}^{C_C}, P_{\hat{w}|w}^{C_C}] + \alpha(m) \cdot [Q_{R,y}^{(m)}; Q_{R,w}^{(m)}] \right) \\ &\quad - \left([P_{\hat{y}|y}^{C_C}, P_{\hat{w}|w}^{C_C}] + \alpha(m) \cdot [Q_{R,y}^{(m)}; Q_{R,w}^{(m)}] \right). \end{aligned} \quad (31d)$$

These equations are similar to (7), with the restraining factor $\alpha(m)$ introduced.

The Jacobian structure for both the row and column decoders will be of the form in (18). For example, the row Jacobian will have n_C square submatrices $J_{x,y}^{C_R,i}$ of size n_R on the main diagonal.

Applying the chain derivation rule, it can be shown that the multiplication by the set of restraining factors is equivalent to multiplying the Jacobian and stability matrices (with all their eigenvalues) by the same factors. Obviously, if the restraining factors are smaller than 1, it improves the stability of the decoding process.

For this decoding scheme, we now show that for asymptotically high SNR, the maximal eigenvalue of S converges to the product of the maximal eigenvalue of the stability matrices of its component codes,

$$\begin{aligned} \lim_{\text{SNR} \rightarrow \infty} |\lambda_S| &= \lim_{\text{SNR} \rightarrow \infty} |\lambda_{S_R}| \cdot \lim_{\text{SNR} \rightarrow \infty} |\lambda_{S_C}| \\ &= (d_R - 1)(d_C - 1), \end{aligned} \quad (32)$$

where λ_S , λ_{S_R} , λ_{S_C} denote the maximal eigenvalues of S , S_R , S_C , respectively.

At the limit, the maximal eigenvalues of both S_R and S_C have the same eigenvector. From [12], if two matrices have the same eigenvector, then their product matrix will have the same eigenvector with the product of the respective eigenvalues as the eigenvalue associated to it.

Repetition code

To illustrate the above, we will examine the same example codes. For the repetition code, we get the following stability matrices (again, each matrix is indexed by rows or columns as is most convenient, the restraining factor is set to 1):

$$S_R = \left[\begin{array}{ccc|cc} 0 & 1 & 1 & & \\ 1 & \ddots & 1 & 0 & 0 \\ 1 & 1 & 0 & & \\ \hline \underbrace{\quad\quad\quad}_{n_R=d_R} & & & & \\ 0 & \ddots & 0 & & \\ \hline & & & 0 & 1 & 1 \\ 0 & 0 & 1 & \ddots & 1 \\ & & 1 & 1 & 0 \\ \hline \underbrace{\quad\quad\quad}_{n_C \cdot n_R = d_C \cdot d_R} & & & \underbrace{\quad\quad\quad}_{n_R=d_R} & & \end{array} \right], \quad (33a)$$

$$S_C = \left[\begin{array}{ccc|cc} 0 & 1 & 1 & & \\ 1 & \ddots & 1 & 0 & 0 \\ 1 & 1 & 0 & & \\ \hline \underbrace{\quad\quad\quad}_{n_C=d_C} & & & & \\ 0 & \ddots & 0 & & \\ \hline & & & 0 & 1 & 1 \\ 0 & 0 & 1 & \ddots & 1 \\ & & 1 & 1 & 0 \\ \hline \underbrace{\quad\quad\quad}_{n_R \cdot n_C = d_R \cdot d_C} & & & \underbrace{\quad\quad\quad}_{n_C=d_C} & & \end{array} \right]. \quad (33b)$$

For $\alpha(m) = 1$, both S_R and S_C are unstable, regardless of the SNR, since they have maximal eigenvalues of $d_R - 1$, and $d_C - 1$, respectively. Therefore, the overall decoding process is unstable.

A code with 2×2 information bits

We now examine the second example and use a code with two information bits and a single check bit for both the row and the column codes (note that this is a private case of the example shown for Benedetto's decoder). The rows matrix

form is

$$S_R = \left[\begin{array}{ccc|ccc|ccc} 0 & \gamma_{1,2} & \gamma_{1,3} & 0 & 0 & 0 & 0 & 0 & 0 \\ \gamma_{2,1} & 0 & \gamma_{2,3} & 0 & 0 & 0 & 0 & 0 & 0 \\ \gamma_{3,1} & \gamma_{3,2} & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ \hline 0 & 0 & 0 & 0 & \gamma_{4,5} & \gamma_{4,6} & 0 & 0 & 0 \\ 0 & 0 & 0 & \gamma_{5,4} & 0 & \gamma_{5,6} & 0 & 0 & 0 \\ 0 & 0 & 0 & \gamma_{6,4} & \gamma_{6,5} & 0 & 0 & 0 & 0 \\ \hline 0 & 0 & 0 & 0 & 0 & 0 & 0 & \gamma_{7,8} & \gamma_{7,9} \\ 0 & 0 & 0 & 0 & 0 & 0 & \gamma_{8,7} & 0 & \gamma_{8,9} \\ 0 & 0 & 0 & 0 & 0 & 0 & \gamma_{9,7} & \gamma_{9,8} & 0 \end{array} \right]. \quad (34a)$$

The column stability matrix, indexed by the columns, has the same form, but if we index the matrix by the rows (as the row decoder Jacobian is ordered) it becomes

$$S_C = \left[\begin{array}{ccc|ccc|ccc} 0 & 0 & 0 & \mu_{1,4} & 0 & 0 & \mu_{1,7} & 0 & 0 \\ 0 & 0 & 0 & 0 & \mu_{2,5} & 0 & 0 & \mu_{2,8} & 0 \\ 0 & 0 & 0 & 0 & 0 & \mu_{3,6} & 0 & 0 & \mu_{3,9} \\ \hline \mu_{4,1} & 0 & 0 & 0 & 0 & 0 & \mu_{4,7} & 0 & 0 \\ 0 & \mu_{5,2} & 0 & 0 & 0 & 0 & 0 & \mu_{5,8} & 0 \\ 0 & 0 & \mu_{6,3} & 0 & 0 & 0 & 0 & 0 & \mu_{6,9} \\ \hline \mu_{7,1} & 0 & 0 & \mu_{7,4} & 0 & 0 & 0 & 0 & 0 \\ 0 & \mu_{8,2} & 0 & 0 & \mu_{8,5} & 0 & 0 & 0 & 0 \\ 0 & 0 & \mu_{9,3} & 0 & 0 & \mu_{9,6} & 0 & 0 & 0 \end{array} \right]. \quad (34b)$$

As explained before, both these matrices are marginally stable at high SNRs, and the stability of the process is determined through their product. Generally, for other codes, this decoding process will be unstable at high SNR's, as practical codes have $d \geq 2$. The restraining factor can be used to stabilize the iterative process of some of the iterations.

6. SIMULATION RESULTS

We simulated Benedetto's and Pyndiah's decoding schemes for two SCPC: Hamming $[(7, 4, 3)]^2$ and Golay $[(24, 12, 8)]^2$. Since the results for both codes have similar phenomena, we preferred to present the Golay $[(24, 12, 8)]^2$ results for Benedetto decoding scheme, and the Hamming $[(7, 4, 3)]^2$ for Pyndiah's.

MAP decoders were used as the component decoders of the rows and columns codes (note that Pyndiah originally used the suboptimal Chase decoder). Also, for comparison, we simulated the decoding algorithm for the corresponding PCPC.

For a given SNR (AWGN channel), we simulated the transmission of encoded blocks. For each block we ran up to 10 decoding iterations, in which we computed the BER, the stability matrices S , S_R , S_C , and their maximal eigenvalues.

As expected, due to the SDM's instability, we had to address out-of-bound numerical results in the decoding process, as the density of some bits overflowed. In these cases, we chose to stop the decoding, and discard the results of the last iteration. Hence, we had a significant reduction in the simulation data ensemble for the last iterations. Note that for practical implementations, a different stopping criterion should be considered.

Figure 2 presents the results obtained for turbo decoding of the Golay PCPC and serves as a comparison reference to the decoding of SCPC. The figure shows the maximal eigenvalue of the stability matrix of the row and column decoders, as well as the overall decoder. The simulations show that as the SNR grows, the maximal eigenvalue approaches zero. Also evident is that the maximal eigenvalue of the overall stability matrix is within order of magnitudes smaller than the maximal eigenvalues of the row and column decoders (refer to [4, 5] for explanation of this phenomena).

Figure 3 shows the maximal eigenvalue of the stability matrices of the outer, inner, and overall decoders of Benedetto's scheme for the Golay code. The outer decoder (which is not an SDM-type decoder) is stable: its maximal eigenvalue converges to zero. As for the inner (SDM) decoder, its maximal eigenvalue approaches $d - 1 = 7$ (where d is the minimum Hamming distance of the Golay component code)—as was predicted by the theoretical results. The overall decoding process is stable again, due to the stability of the outer decoder (although the eigenvalues approach zero in a slower rate, compared to the rate of the corresponding parallel concatenation decoders, presented in Figure 2).

Figure 4 shows the maximal eigenvalue of the stability matrices of the outer, inner, and overall decoder in Pyndiah's scheme for the Hamming code. Here, both decoders are of SDM type and their maximal eigenvalue approaches $d - 1 = 2$. Moreover, the eigenvalues of the overall stability matrix approach $(d_R - 1)(d_C - 1) = 4$ (as explained before), and the decoder is unstable. Note that compared to the turbo decoding of PCPC, in which the overall stability matrix had a much smaller maximal eigenvalue compared to those of the component decoders, here the contrary occurs: S has larger eigenvalues compared to those of the component codes.

The effect of applying restraining factors to Pyndiah's scheme is presented in Figure 5 for Hamming code. We used the same set of restraining factors used in [6]: $\alpha(m) = [0, 0.2, 0.3, 0.5, 0.7, 0.9, 1, 1]$. Note that these values were selected for the particular code used there, and we did not try to optimize the factors nor to use them to force the decoder to converge. Thus, the effect of the restraining factors is limited in our simulation. The maximal eigenvalues of the first iterations are decreased due to this multiplication, and converge to $\alpha(m)(d - 1)$.

As can be seen, the simulation results support the theoretical results. The maximal eigenvalue of the SDM's stability matrix for the Hamming and Golay codes approaches $d - 1$ and the SDM-type decoder is indeed inherently unstable.

7. CONCLUSION

We extended the framework, established by Richardson, for turbo decoding of serially concatenated block codes and turbo codes. General update equations were derived for this case, and we showed how they are linked to the decoding algorithms of Benedetto and Pyndiah. The main difference, compared to decoding of parallel-concatenated code, is the incorporation of the SDM, in which the extrinsic information is calculated also for the code's check bits.

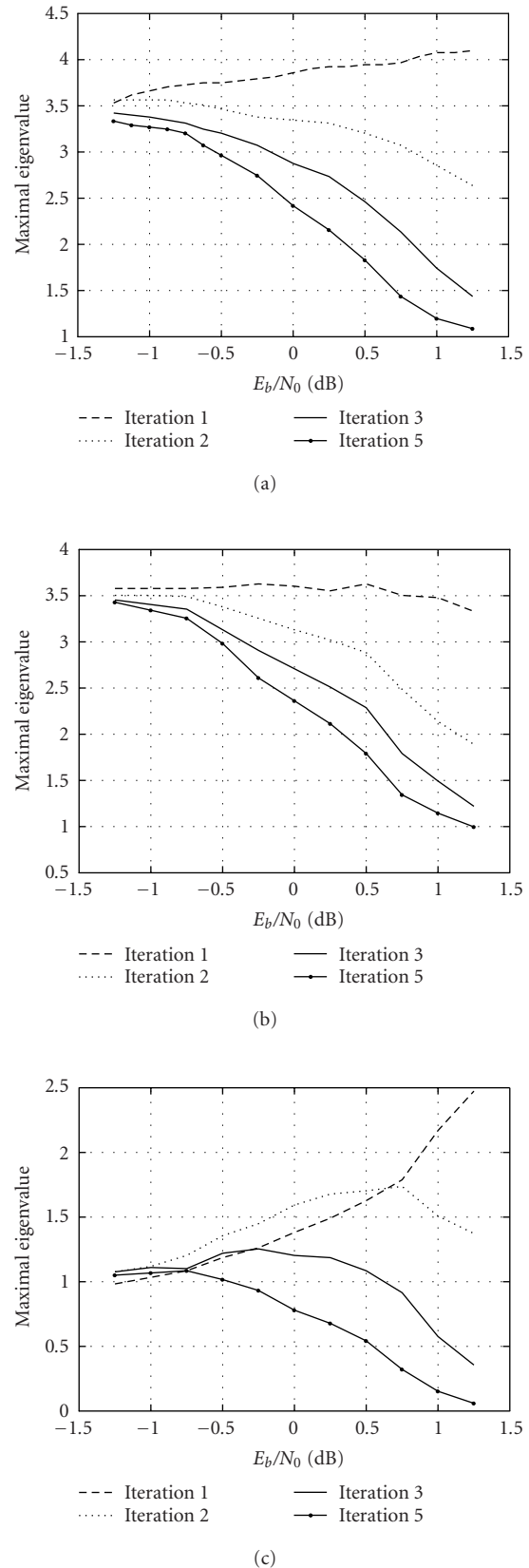


FIGURE 2: PCPC scheme—averaged maximal eigenvalue of (a) S^R , (b) S^C , and (c) S for Golay $[24, 12, 8]^2$.

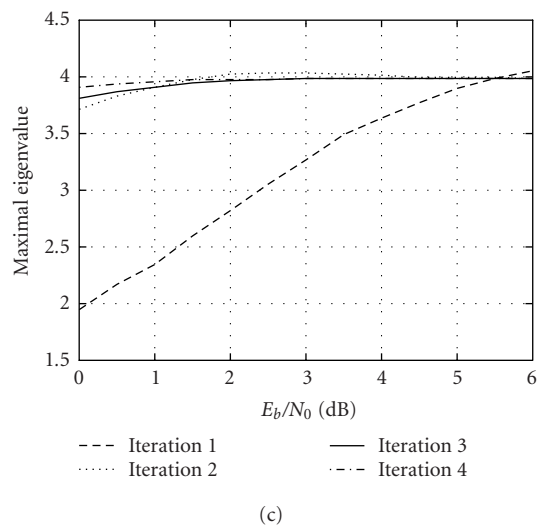
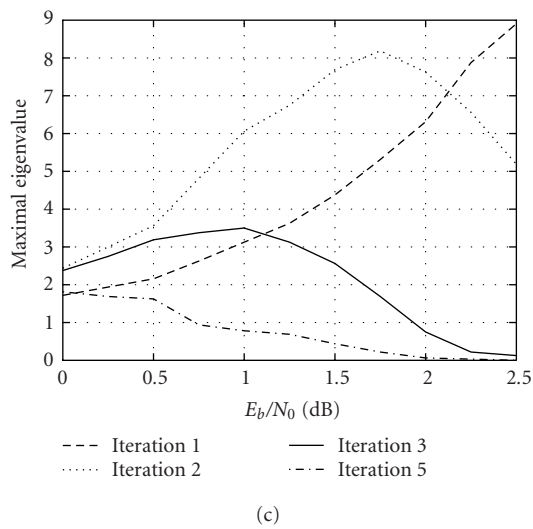
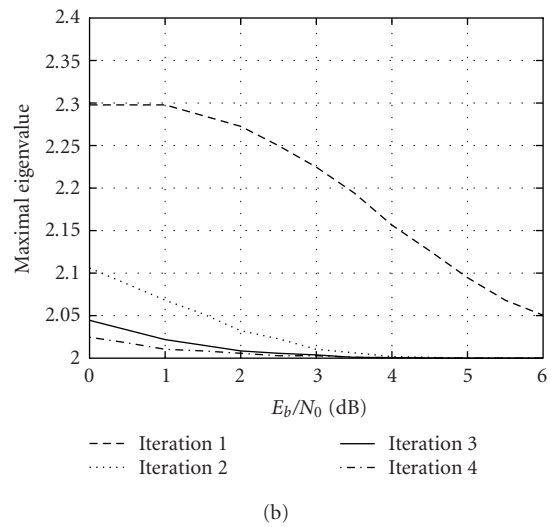
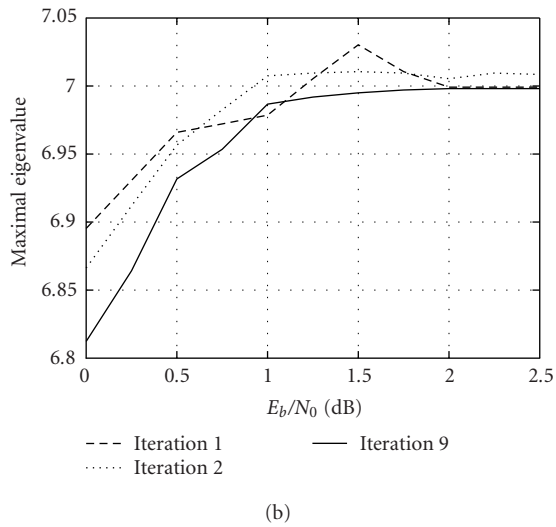
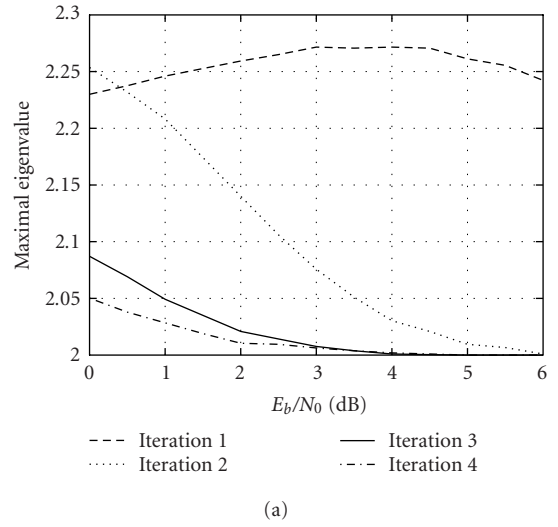
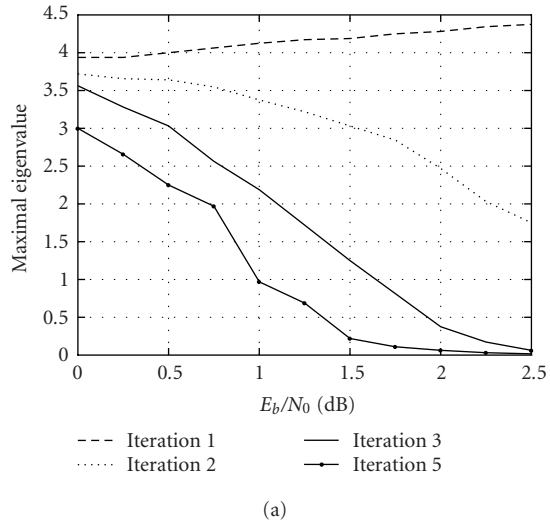


FIGURE 3: Benedetto's scheme—averaged maximal eigenvalue of (a) S^R , (b) S^C , and (c) S for Golay $[24, 12, 8]^2$.

FIGURE 4: Pyndiah's scheme—averaged maximal eigenvalue of (a) S^R , (b) S^C , and (c) S for Hamming $[7, 4, 3]^2$.

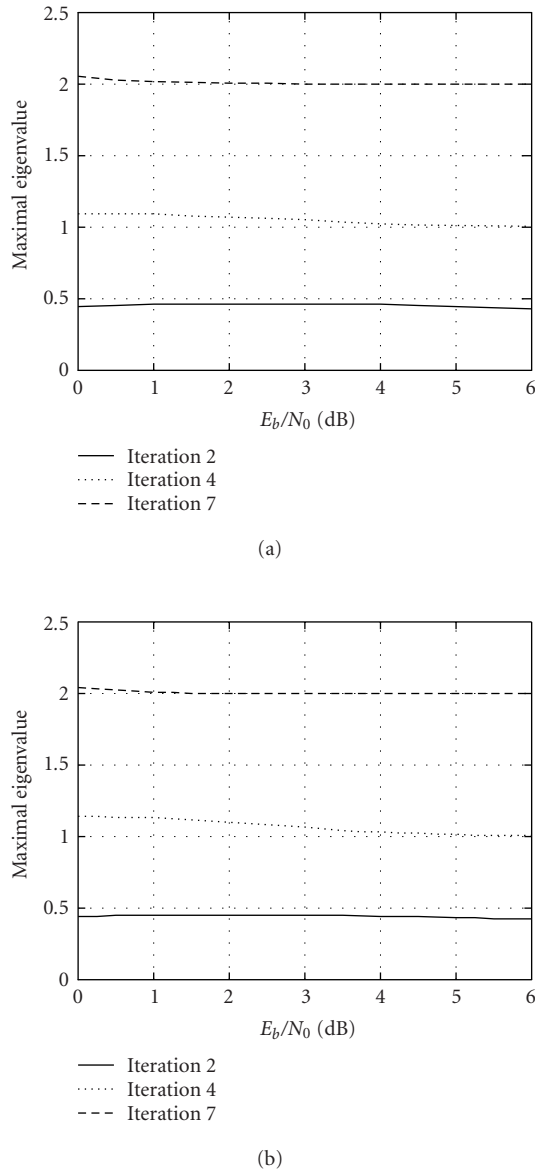


FIGURE 5: Pyndiah's scheme with restraining factors—averaged maximal eigenvalue of (a) S^R and (b) S^C for Hamming $[7, 4, 3]^2$.

Then, we investigated the stability of the SDM, and of the overall decoder. For some simple codes we demonstrated that the extrinsic information, calculated by the SDM decoder, does not converge throughout the iterative process. Moreover, when the SNR is high the decoder becomes over confident in its decisions, and the extrinsic information approaches $\pm\infty$. Here, we showed a connection between the eigenvalues of the stability matrices and the minimum Hamming distance of the code (d): we proved that the eigenvalues of the SDM's stability-matrix approach $d - 1$, and when two SDMs are incorporated, as in Pyndiah's scheme, they approach $(d_R - 1)(d_C - 1)$. Finally, we provided a theoretical justification for the use of restraining factors in Pyndiah's algorithm.

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Special Issue on Transforming Signal Processing Applications into Parallel Implementations

Call for Papers

There is an increasing need to develop efficient “system-level” models, methods, and tools to support designers to quickly transform signal processing application specification to heterogeneous hardware and software architectures such as arrays of DSPs, heterogeneous platforms involving microprocessors, DSPs and FPGAs, and other evolving multiprocessor SoC architectures. Typically, the design process involves aspects of application and architecture modeling as well as transformations to translate the application models to architecture models for subsequent performance analysis and design space exploration. Accurate predictions are indispensable because next generation signal processing applications, for example, audio, video, and array signal processing impose high throughput, real-time and energy constraints that can no longer be served by a single DSP.

There are a number of key issues in transforming application models into parallel implementations that are not addressed in current approaches. These are engineering the application specification, transforming application specification, or representation of the architecture specification as well as communication models such as data transfer and synchronization primitives in both models.

The purpose of this call for papers is to address approaches that include application transformations in the performance, analysis, and design space exploration efforts when taking signal processing applications to concurrent and parallel implementations. The Guest Editors are soliciting contributions in joint application and architecture space exploration that outperform the current architecture-only design space exploration methods and tools.

Topics of interest for this special issue include but are not limited to:

- modeling applications in terms of (abstract) control-dataflow graph, dataflow graph, and process network models of computation (MoC)
- transforming application models or algorithmic engineering
- transforming application MoCs to architecture MoCs
- joint application and architecture space exploration

- joint application and architecture performance analysis
- extending the concept of algorithmic engineering to architecture engineering
- design cases and applications mapped on multiprocessor, homogeneous, or heterogeneous SOCs, showing joint optimization of application and architecture

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Special Issue on Video Adaptation for Heterogeneous Environments

Call for Papers

The explosive growth of compressed video streams and repositories accessible worldwide, the recent addition of new video-related standards such as H.264/AVC, MPEG-7, and MPEG-21, and the ever-increasing prevalence of heterogeneous, video-enabled terminals such as computer, TV, mobile phones, and personal digital assistants have escalated the need for efficient and effective techniques for adapting compressed videos to better suit the different capabilities, constraints, and requirements of various transmission networks, applications, and end users. For instance, Universal Multimedia Access (UMA) advocates the provision and adaptation of the same multimedia content for different networks, terminals, and user preferences.

Video adaptation is an emerging field that offers a rich body of knowledge and techniques for handling the huge variation of resource constraints (e.g., bandwidth, display capability, processing speed, and power consumption) and the large diversity of user tasks in pervasive media applications. Considerable amounts of research and development activities in industry and academia have been devoted to answering the many challenges in making better use of video content across systems and applications of various kinds.

Video adaptation may apply to individual or multiple video streams and may call for different means depending on the objectives and requirements of adaptation. Transcoding, transmoding (cross-modality transcoding), scalable content representation, content abstraction and summarization are popular means for video adaptation. In addition, video content analysis and understanding, including low-level feature analysis and high-level semantics understanding, play an important role in video adaptation as essential video content can be better preserved.

The aim of this special issue is to present state-of-the-art developments in this flourishing and important research field. Contributions in theoretical study, architecture design, performance analysis, complexity reduction, and real-world applications are all welcome.

Topics of interest include (but are not limited to):

- Heterogeneous video transcoding
- Scalable video coding
- Dynamic bitstream switching for video adaptation

- Signal, structural, and semantic-level video adaptation
- Content analysis and understanding for video adaptation
- Video summarization and abstraction
- Copyright protection for video adaptation
- Crossmedia techniques for video adaptation
- Testing, field trials, and applications of video adaptation services
- International standard activities for video adaptation

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Special Issue on Knowledge-Assisted Media Analysis for Interactive Multimedia Applications

Call for Papers

It is broadly acknowledged that the development of enabling technologies for new forms of interactive multimedia services requires a targeted confluence of knowledge, semantics, and low-level media processing. The convergence of these areas is key to many applications including interactive TV, networked medical imaging, vision-based surveillance and multimedia visualization, navigation, search, and retrieval. The latter is a crucial application since the exponential growth of audiovisual data, along with the critical lack of tools to record the data in a well-structured form, is rendering useless vast portions of available content. To overcome this problem, there is need for technology that is able to produce accurate levels of abstraction in order to annotate and retrieve content using queries that are natural to humans. Such technology will help narrow the gap between low-level features or content descriptors that can be computed automatically, and the richness and subjectivity of semantics in user queries and high-level human interpretations of audiovisual media.

This special issue focuses on truly integrative research targeting of what can be disparate disciplines including image processing, knowledge engineering, information retrieval, semantic, analysis, and artificial intelligence. High-quality and novel contributions addressing theoretical and practical aspects are solicited. Specifically, the following topics are of interest:

- Semantics-based multimedia analysis
- Context-based multimedia mining
- Intelligent exploitation of user relevance feedback
- Knowledge acquisition from multimedia contents
- Semantics based interaction with multimedia
- Integration of multimedia processing and Semantic Web technologies to enable automatic content sharing, processing, and interpretation
- Content, user, and network aware media engineering
- Multimodal techniques, high-dimensionality reduction, and low level feature fusion

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Special Issue on Super-resolution Enhancement of Digital Video

Call for Papers

When designing a system for image acquisition, there is generally a desire for high spatial resolution and a wide field-of-view. To achieve this, a camera system must typically employ small f-number optics. This produces an image with very high spatial-frequency bandwidth at the focal plane. To avoid aliasing caused by undersampling, the corresponding focal plane array (FPA) must be sufficiently dense. However, cost and fabrication complexities may make this impractical. More fundamentally, smaller detectors capture fewer photons, which can lead to potentially severe noise levels in the acquired imagery. Considering these factors, one may choose to accept a certain level of undersampling or to sacrifice some optical resolution and/or field-of-view.

In image super-resolution (SR), postprocessing is used to obtain images with resolutions that go beyond the conventional limits of the uncompensated imaging system. In some systems, the primary limiting factor is the optical resolution of the image in the focal plane as defined by the cut-off frequency of the optics. We use the term “optical SR” to refer to SR methods that aim to create an image with valid spatial-frequency content that goes beyond the cut-off frequency of the optics. Such techniques typically must rely on extensive a priori information. In other image acquisition systems, the limiting factor may be the density of the FPA, subsequent postprocessing requirements, or transmission bitrate constraints that require data compression. We refer to the process of overcoming the limitations of the FPA in order to obtain the full resolution afforded by the selected optics as “detector SR.” Note that some methods may seek to perform both optical and detector SR.

Detector SR algorithms generally process a set of low-resolution aliased frames from a video sequence to produce a high-resolution frame. When subpixel relative motion is present between the objects in the scene and the detector array, a unique set of scene samples are acquired for each frame. This provides the mechanism for effectively increasing the spatial sampling rate of the imaging system without reducing the physical size of the detectors.

With increasing interest in surveillance and the proliferation of digital imaging and video, SR has become a rapidly growing field. Recent advances in SR include innovative algorithms, generalized methods, real-time implementations,

and novel applications. The purpose of this special issue is to present leading research and development in the area of super-resolution for digital video. Topics of interest for this special issue include but are not limited to:

- Detector and optical SR algorithms for video
- Real-time or near-real-time SR implementations
- Innovative color SR processing
- Novel SR applications such as improved object detection, recognition, and tracking
- Super-resolution from compressed video
- Subpixel image registration and optical flow

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Special Issue on

Advanced Signal Processing and Computational Intelligence Techniques for Power Line Communications

Call for Papers

In recent years, increased demand for fast Internet access and new multimedia services, the development of new and feasible signal processing techniques associated with faster and low-cost digital signal processors, as well as the deregulation of the telecommunications market have placed major emphasis on the value of investigating hostile media, such as powerline (PL) channels for high-rate data transmissions.

Nowadays, some companies are offering powerline communications (PLC) modems with mean and peak bit-rates around 100 Mbps and 200 Mbps, respectively. However, advanced broadband powerline communications (BPLC) modems will surpass this performance. For accomplishing it, some special schemes or solutions for coping with the following issues should be addressed: (i) considerable differences between powerline network topologies; (ii) hostile properties of PL channels, such as attenuation proportional to high frequencies and long distances, high-power impulse noise occurrences, time-varying behavior, and strong inter-symbol interference (ISI) effects; (iv) electromagnetic compatibility with other well-established communication systems working in the same spectrum, (v) climatic conditions in different parts of the world; (vii) reliability and QoS guarantee for video and voice transmissions; and (vi) different demands and needs from developed, developing, and poor countries.

These issues can lead to exciting research frontiers with very promising results if signal processing, digital communication, and computational intelligence techniques are effectively and efficiently combined.

The goal of this special issue is to introduce signal processing, digital communication, and computational intelligence tools either individually or in combined form for advancing reliable and powerful future generations of powerline communication solutions that can be suited with for applications in developed, developing, and poor countries.

Topics of interest include (but are not limited to)

- Multicarrier, spread spectrum, and single carrier techniques
- Channel modeling

- Channel coding and equalization techniques
- Multiuser detection and multiple access techniques
- Synchronization techniques
- Impulse noise cancellation techniques
- FPGA, ASIC, and DSP implementation issues of PLC modems
- Error resilience, error concealment, and Joint source-channel design methods for video transmission through PL channels

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Special Issue on Numerical Linear Algebra in Signal Processing Applications

Call for Papers

The cross-fertilization between numerical linear algebra and digital signal processing has been very fruitful in the last decades. The interaction between them has been growing, leading to many new algorithms.

Numerical linear algebra tools, such as eigenvalue and singular value decomposition and their higher-extension, least squares, total least squares, recursive least squares, regularization, orthogonality, and projections, are the kernels of powerful and numerically robust algorithms.

The goal of this special issue is to present new efficient and reliable numerical linear algebra tools for signal processing applications. Areas and topics of interest for this special issue include (but are not limited to):

- Singular value and eigenvalue decompositions, including applications.
- Fourier, Toeplitz, Cauchy, Vandermonde and semi-separable matrices, including special algorithms and architectures.
- Recursive least squares in digital signal processing.
- Updating and downdating techniques in linear algebra and signal processing.
- Stability and sensitivity analysis of special recursive least-squares problems.
- Numerical linear algebra in:
 - Biomedical signal processing applications.
 - Adaptive filters.
 - Remote sensing.
 - Acoustic echo cancellation.
 - Blind signal separation and multiuser detection.
 - Multidimensional harmonic retrieval and direction-of-arrival estimation.
 - Applications in wireless communications.
 - Applications in pattern analysis and statistical modeling.
 - Sensor array processing.

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Special Issue on

Wavelets in Source Coding, Communications, and Networks

Call for Papers

Wavelet transforms are arguably the most powerful, and most widely-used, tool to arise in the field of signal processing in the last several decades. Their inherent capacity for multiresolution representation akin to the operation of the human visual system motivated a quick adoption and widespread use of wavelets in image-processing applications. Indeed, wavelet-based algorithms have dominated image compression for over a decade, and wavelet-based source coding is now emerging in other domains. For example, recent wavelet-based video coders exploit techniques such as motion-compensated temporal filtering to yield effective video compression with full temporal, spatial, and fidelity scalability. Additionally, wavelets are increasingly used in the source coding of remote-sensing, satellite, and other geospatial imagery. Furthermore, wavelets are starting to be deployed beyond the source-coding realm with increasing interest in robust communication of images and video over both wired and wireless networks. In particular, wavelets have been recently proposed for joint source-channel coding and multiple-description coding. This special issue will explore these and other latest advances in the theory and application of wavelets.

Specifically, this special issue will gather high-quality, original contributions on all aspects of the application of wavelets and wavelet theory to source coding, communications, and network transmission of images and video. Topics of interest include (but are not limited to) the theory and applications of wavelets in:

- Scalable image and video coding
- Motion-compensated temporal filtering
- Source coding of images and video via frames and overcomplete representations
- Geometric and adaptive multiresolution image and video representations
- Multiple-description coding of images and video
- Joint source-channel coding of images and video
- Distributed source coding of images and video
- Robust coding of images and video for wired and wireless packet networks

- Network adaption and transcoding of images and video
- Coding and communication of images and video in sensor networks

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