Advance Communication Center

Workshop on

RFIC and Advanced RF Design

Keynotes speaker

Professor Yannis Papananos,

National Technical University of Athens

July 2nd, 2008; 8:30-17:00 at

Rosenblatt Hall, School of Electrical Engineering, Tel Aviv University

Next generation wireless systems will require a high level of integrations and therefore a high level of sofisticaaations.

The goal of this workshop is to bring together researchers from academia and practitioners from industry to present developments, research and contributions related to advanced RF architectures in RFIC.

The Advanced Communication Center - ACC - has been established at the Electrical Engineering School, Tel-Aviv University, with an initial grant from Intel Corporation. Utilizing the worldwide reputation of its researchers in communications and information technologies, and the strong industry involvement, the center will advance interdisciplinary research and teaching in the communication area.

Updates and registration at www.acc.tau.ac.il

The participation is free of charge but registration is very much appreciated in order to enable us to make proper preparations. The program will show up in the site.

Program

Keynote speaker

- Professor **Yannis Papananos**, National Technical University of Athens, **Silicon RFIC Design: From Bipolar to nm-scale CMOS - An Overview**

Prof. Yannis Papananos

Scientific Advisor

Dr. Papananos holds the Dipl. Eng. and PhD degrees from the National Technical University of Athens, together with an MS degree from Columbia University. Dr. Papananos is currently Associate Professor and head of the Microelectronic Circuit Design Group at NTUA. He has consulted with numerous electronics companies in both Europe and the US and is a co-founder of Theta S.A. His main areas of research interest are analog VLSI design and CAD for VLSI design, with emphasis in silicon RFIC design. Dr. Papananos has authored and co-authored over 35 technical papers and two books on RFIC and Active Analog Filter design. He is also a senior member of the IEEE, serving on a number of their committees and a member of the International Educational Advisory Board of Hewlett Packard.

Invited speakers (confirmed)

- **Gadi Shirazi**, Motorola Israel, **Modern Transceiver Architecture with respect to RFIC design**
- **Dr. Ofir Degani,** Intel - IDC Haifa **A 1×2 MIMO Multi-band Transceiver with an Integrated LNA and PA in 90nm CMOS for 802.11a/g/n Applications**
- **David Gidony,** Intel Mobility Wireless Group, **Amplifier design challenges in 45nm CMOS process, within low voltage and digital transistors regime.**
- **Dr. Solon Spiegel,** RIO systems, **Quantization noise in sigma delta fractional-N synthesizers: Theory and implementation**
- **Prof. Yosi Pinhasi**, Ariel University Center of Samaria, **TeraHertz Frontier**
- **Dror Regev,** Elipse RFIC Array Devices**, Inductors in RFIC.**
- **Dr. Ofer Amrani**, Tel Aviv University, **Optical Digital to Analog Converter**
- **Dr. Miki Moyal,** Intel, **Recent Advances in High Speed AtoDs**
- **Erez Sarig**, Tower Semiconductors, **Mixed signal design in RFIC**